

ABSTRACT

The invention relates to a method and device for the conversion of digital signals comprising a phase involving modulation with the aid of a vector lattice encoder. The inventive method comprises iterative steps which are performed on N output candidates, consisting in filtering (Hx , Hq), determining the difference between the filtered signals calculating two possible evolutions for said options, pre-selecting the candidates that minimise the difference, weighting the difference with a cost function (\mathcal{W}), marking the candidates eliminated for a subsequent iteration, and selecting the best candidate over a period determined by a historical decision dimension. The invention also relates to the use thereof in relation to a digital audio signal amplifier